

## PATENT AND TRADEMARK OFFICE

CLASS NO.

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PAGE 1 OF 1

APPLICANT(S)

Barinder Singh Rai, et al.

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Not Yet Assigned

## U.S. PATENT DOCUMENTS

| E.I.      | DOCUMENT NUMBER | DATE     | NAME                 | CLASS | SUBCLASS | FILING DATE |
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| <i>AF</i>                   | AT | Thomas Gleeup, et al., "Memory Architecture for Efficient Utilization of SDRAM: A Case Study of the Computation/Memory Access Trade-Off", <i>Proceedings of the Eighth International Workshop on Hardware/Software Codesign</i> , 2000, pp. 51-55 |
| EXAMINER <i>[Signature]</i> |    | DATE CONSIDERED 03/13/06  |

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